IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Takahiro IMAI of Yamagata –ken, Japan Serial No: Not assigned Filed: September 12, 2003 For: Semiconductor Device and Method of Manufacturing the Same, Circuit Board, and Electronic Instrument								22278 US PTO	
Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450									
Dear Sir: CORRESPONDENCE ADDRESS: ☐ Customer Number ☐ 000026021 ☐ 00002602									
Transmitted herewith for filing is the patent application identified above.									
	ITEM	TOTAL NO. OF	CULA1	NO. OF CLAIMS	LG/S	M	\$ AMOUNT	\$ FEE	
A	TOTAL CLAIMS FE E	CLAIMS 36	-20	OVER BASE	\$ ENTIT	Y FEE \$18	288	3 FEE	
В	INDEPENDENT CLAIMS FEE*	2	-3	0	SM=\$9 LG=\$84	\$84	\$ 0		
c	SUBTOTAL - ADDITIONAL CLAIM	L	لمستمل	LINES A + B)	SM=\$42		L	288	
D	MULTIPLE-DEPENDENT CLAIMS						TY FEE = \$280	\$ 0	
E	BASIC FEE				LAR	GE ENTI	TY FEE = \$140 TY FEE = \$750	\$ 750	
F	F TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E) \$ 1038							\$ 1038	
G							\$ 40		
<u>L</u> _	*LIST INDEPENDENT CLAIMS 1 and 20.								
 ☐ Please charge my Deposit Account No. 50-1314 the amount of \$_0 A copy of this letter is enclosed. ☐ A check in the amount of \$_0_ to cover the filing fee is enclosed. ☐ A check in the amount of \$_40.00_ to cover Assignment Recordation fee is enclosed. ☐ The Commissioner is hereby authorized to charge any deficiency for the following fees associated with this communication or credit any overpayment to Deposit Account No. 50-1314. A copy of this sheet is enclosed. ☐ Any additional filing fees required under 37 C.F.R. 1.16 ☐ Any patent application processing fees under 37 C.F.R. 1.17 									
Respectfully/submitted//									
Date: September 12, 2003 By: Anthony J. Offer									
Los A	500 South Grand Avenue, Suite 1900 Los Angeles, CA 90071 Telephone: (213) 337-6700 Facsimile: (213) 337-6701 Registration Mg. 41,232 Attorney for Applicant(s)								

PATENT

Attorney Docket No: 81751.0065

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In re ap	pplication of:	Art Unit:	Not assigned			
Takahir	ro IMAI	Examiner:	Not assigned			
Serial N	No: Not assigned					
Filed:	September 12, 2003					
For:	Semiconductor Device and Method of Manufacturing Same, Circuit Board, and Electronic Instrument	the				
	CERTIFICATE OF MAILING VIA U.S "Express Mail" Mailing Label No. E\ Date of Deposit: September	V 325 217 390 US				
Commi P.O. Bo	op PATENT APPLICATION ssioner for Patents ox 1450 dria, VA 22313-1450					
Dear S	ir:					
	I hereby certify that	tion No. <u>2002-27</u> claimed in the sub rence	7 <u>7454</u> which was filed ject case pursuant to 35			
are be	ing deposited with the United States Postal Service with sufficient postage under 37 C.F.R. § 1.10 on the	e "Express Mail Pod date indicated abov	ost Office to Addressee ve and are addressed to:			
	Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Arlington, VA 22313-1450					
Date: September 12, 2003		Roeshene Carbonilla				
Hogan & Hartson, LLP 500 South Grand Avenue, Suite 1900 Los Angeles, California 90071 Telephone: 213-337-6700 Facsimile: 213-337-6701		Name of person	bowller			